

MacPherson Kwok Chen & Heid LLP

1762 Technology Drive, Suite 226

San Jose, CA 95110

Tel. (408) 392-9250

Fax (408) 392-9262

2402 Michelson Drive, Suite 210

Irvine, CA 92612

Tel. (949) 752-7040

Fax (949) 752-7049

Email: mailbox@macpherson-kwok.com

www.macpherson-kwok.com

22386 U.S. PTO
10/660814
09/12/03

Atty. Docket No.: M-15198 US

September 12, 2003

Mail Stop Patent Application
Commissioner For Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Enclosed herewith for filing is a patent application, as follows:

Inventor(s): Satwant Singh & Cyrus Tsui

Title: Delay-Matched ASIC Conversion Of A Programmable Logic Device

X Return Receipt Postcard
X This Transmittal Letter (in duplicate)
X Twelve (12) page(s) Specification (not including claims)
X Nine (9) page(s) Claims
X One (1) page Abstract
X Five (5) sheet(s) of Drawings including Figures 1, 2, 3, 4a, 4b, 5, 6, and 7
X Two (2) page(s) Declaration For Patent Application and Power of Attorney
X Three (3) page(s) Assignment and Recordation Form Cover Sheet
X One (1) page Non-Publication Request

CLAIMS AS FILED

<u>For</u>	<u>Number</u>		<u>Number</u>		<u>Rate</u>		<u>Basic Fee</u>
	<u>Filed</u>		<u>Extra</u>				
Total Claims	37	-20 =	17	X	\$18.00	=	\$ 306.00
Independent Claims	5	-3 =	2	X	\$84.00	=	\$ 168.00
<input type="checkbox"/>	Fee of _____ for the first filing of one or more multiple dependent claims per application						\$
<input type="checkbox"/>	Fee for Request for Extension of Time						\$

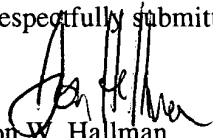
Please make the following charges to Deposit Account No. 50-2257:

- ☒ Total fee for filing the patent application in the amount of \$ 1,224.00
☒ The Commissioner is hereby authorized to charge any additional fees which may be required, or credit any overpayment to Deposit Account No. 50-2257.

EXPRESS MAIL LABEL NO:

EV252520996US

Respectfully submitted,


Jon W. Hallman
Attorney for Applicants
Reg. No. 42,622

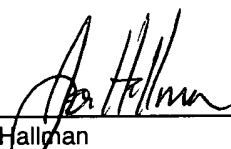
EXPRESS MAIL LABEL NO:
EV252520996US

MODIFIED PTO/SB/35 (11-00)

REQUEST AND CERTIFICATION UNDER 35 U.S.C. 122(b)(2)(B)(i)	Inventor	Satwant Singh & Cyrus Tsui
	Title	Delay-Matched Version Of A Programmable IC With Built-In Testability
	Atty Docket Number	M-15198 US

I hereby certify that the invention disclosed in the attached application **has not and will not be** the subject of an application filed in another country, or under a multilateral agreement, that requires publication at eighteen months after filing. I hereby request that the attached application not be published under 35 U.S.C. 122(b).

September 12, 2003
Date


Jon W. Hallman
Attorney for Applicants
Reg. No.: 42,622

This request must be signed in compliance with 37 CFR 1.33(b) and submitted with the application **upon filing**.

Applicant may rescind this nonpublication request at any time. If applicant rescinds a request that an application not be published under 35 U.S.C. 122(b), the application will be scheduled for publication at eighteen months from the earliest claimed filing date for which a benefit is claimed.

If applicant subsequently files an application directed to the invention disclosed in the attached application in another country, or under a multilateral international agreement, that requires publication of applications eighteen months after filing, the applicant **must** notify the United States Patent and Trademark Office of such filing within forty-five (45) days after the date of the filing of such foreign or international application. **Failure to do so will result in abandonment of this application (35 U.S.C. 122(b)(2)(B)(iii)).**